TASTE tool-chain demo

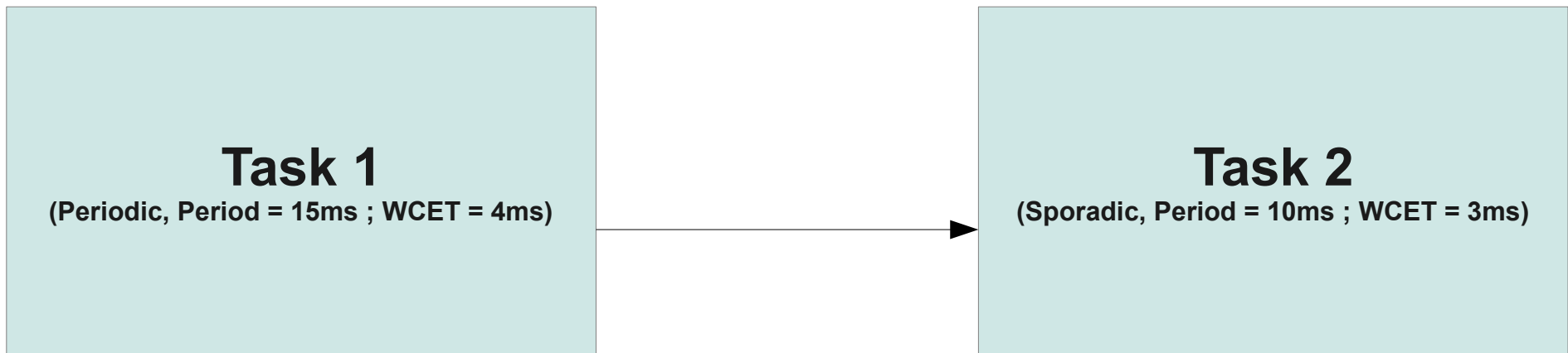**Julien Delange <julien.delange@esa.int>**

Maxime Perrotin <maxime.perrotin@esa.int>

Architecture

Functional

Activator interface
(Periodic, Period = 15ms ; WCET = 4ms)
Sender function
Required interface

Receiver function
Receive_int interface
(Sporadic, Period = 10ms ; WCET = 3ms)

Concurrency

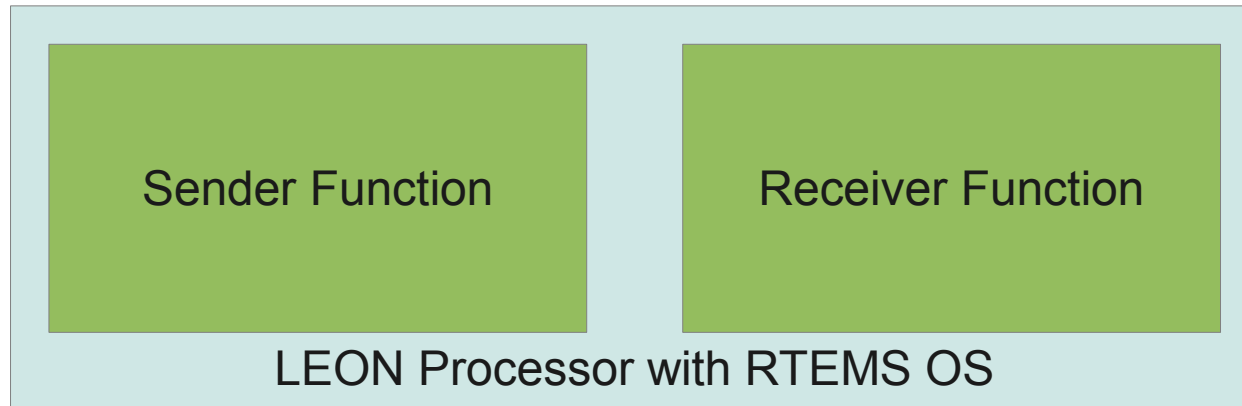| Task 1	Task 2
(Periodic, Period = 15ms ; WCET = 4ms)	**(Sporadic, Period = 10ms ; WCET = 3ms)**

First Deployment
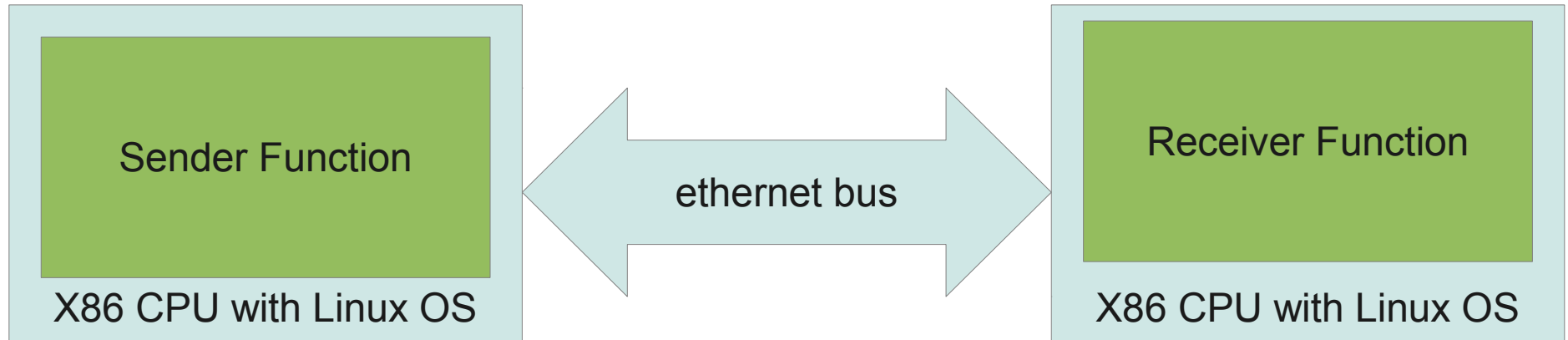

Sender Function   Receiver Function

LEON Processor with RTEMS OS

- Scheduling analysis & validation
- Automatic generation of SPARC binaries
- Execution using QEMU
- Code coverage analysis

Second Deployment

- Function distribution over ethernet bus
- Automatic generation of x86 binaries
- Behavior analysis

http://www.assert-project.net/taste